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APPLICATION NO. FILING DATE	FIRST NAMED INVENTOR	ATTORNEY_DOCKET NO.
 - 08/691.434 - 08/02/96	YAMAZAKI	· · ·
022204 NIXON FEABODY, LLP	MM92/0424 —	EXAMINER WILCZEWSKI, M
8180 GREENSBORO DRIVE SUITE 800		ART UNIT PAPER NUMBER
MCLEAN VA 22102		ARTUNIT PAPER NUMBER
		04/24/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.

Applicant(s)

08/691,434

Yamazaki et al.

Examiner

M. Wilczewski

t Unit 2822

The MAILING DATE of this communication appears on the cover sheet with the correspondence address					
Period fo	• •				
THE M	PRTENED STATUTORY PERIOD FOR REPLY IS SET IAILING DATE OF THIS COMMUNICATION.	· ·			
afte	er SIX (6) MONTHS from the mailing date of this communic				
 If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. 					
	- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.				
- Failure - Any re	e to reply within the set or extended period for reply will, by	statute, cause the application to become ABANDONED (35 U.S.C. § 133). mailing date of this communication, even if timely filed, may reduce any			
Status					
1) 💢	Responsive to communication(s) filed on <u>Jan 31, 2</u>	001			
2a) 💢	This action is FINAL . 2b) This action is non-final.				
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11; 453 O.G. 213.				
Disposit	ion of Claims				
4) 💢	Claim(s) <u>16-20, 24, 25, 56-61, and 74-91</u>	is/are pending in the application.			
4	a) Of the above, claim(s)	is/are withdrawn from consideration.			
5) 🗆	Claim(s)	is/are allowed.			
6) 💢	Claim(s) 16-20, 24, 25, 56-61, and 74-91	is/are rejected.			
7) 🗆	Claim(s)	is/are objected to.			
8) 🗆	Claims	are subject to restriction and/or election requirement.			
Applicat	ion Papers				
9) The specification is objected to by the Examiner.					
10)	The drawing(s) filed on is/are objected to by the Examiner.				
11)	☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved.				
12)					
Priority	under 35 U.S.C. § 119				
13) Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).					
a) ☑ All b) ☐ Some* c) ☐ None of:					
1	1. Certified copies of the priority documents have been received.				
2	2. Certified copies of the priority documents have been received in Application No				
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).					
*See the attached detailed Office action for a list of the certified copies not received.					
14)∐	Acknowledgement is made of a claim for domestic	priority under 35 U.S.C. § 119(e).			
Attachme	ent(s)				
15) No	tice of References Cited (PTO-892)	18) Interview Summary (PTO-413) Paper No(s).			
		19) Notice of Informal Patent Application (PTO-152)			
17) Information Disclosure Statement(s) (PTO-1449) Paper No(s). 48,49 20) Other:					

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DETAILED ACTION

Continued Prosecution Application

The request filed on May 15, 2000, for a Continued Prosecution Application (CPA) under 37 CFR 1.53(d) based on parent Application No. 08/691,434 is acceptable and a CPA has been established. An action on the CPA follows.

Priority

Acknowledgment is made of applicant's claim for foreign priority under 35 U.S.C. 119(a)-(d). The certified copy has been filed in parent Application No. 08/160,909, filed on February 18, 1994.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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Claims 16-20, 24, 25, 80 and 86 are rejected under 35 U.S.C. 103(a) as being unpatentable over Begin et al. in view of Miyachi et al., Nakayama et al., and Kawasaki et al., further in view of Codama, all of record, further in view of Pressley, U.S. Patent 4,475,027, further in view of Kawachi et al., the article entitled "Large-Area Process for Fabrication of Poly-Si Thin Film Transistors Using Bucket Ion Source and XeCl Excimer Laser Annealing", newly cited.

Begin et al. disclose an apparatus for processing semiconductor wafers which includes satellite reaction chambers 60, 62, 64, and 66 disposed around the periphery of central chamber 14, see figure 1. A robot assembly 16 comprising arms 18, 20, and 22 is disposed in central chamber 14. Assembly 16 moves the substrate 12 to any position within the apparatus. Begin et al. lack anticipation only of disclosing that reaction chambers 60, 62, 64, and 66 comprise an ion introducing apparatus and a laser processing apparatus. However, apparatuses used for irradiating an amorphous silicon layer for dehalogenating and hydrogenating the layer, etching, and plasma doping are well known in the art, see Miyachi et al., Kawasaki et al., and Nakayama et al., respectively.

Miyachi et al., in particular, disclose an apparatus which comprises a film-forming chamber 1 for forming an amorphous semiconductor film and a dehalogenating-hydrogenating chamber 2, see figure 5, for example. The two chambers are combined by a conveying device 13. The substrates 10 move between the two chambers without being exposed to

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outside air. Note in Example 14 that the dehalogenation-hydrogenation is preferably performed by light irradiation using, for example, an ultraviolet laser, a visible light laser, or a carbon dioxide laser, see column 18, lines 29-43. Miyachi et al. lack anticipation only of using a rectangular-shaped laser beam having an elongated cross-section and of moving the substrate in a direction orthogonal to the laser beam during the irradiating step.

Pressley discloses an laser processing apparatus which comprises a rectangularly-shaped laser beam in which the laser beam is scanned by moving the beam relative to the substrate, see column 7, lines 4-9. Since the apparatus of Pressley permits uniform laser irradiation of semiconducting materials, it would have been obvious to one of ordinary skill in the art to use a laser beam having a rectangular cross-section in the dehalogenating-hydrogenating chamber of Miyachi et al. In addition, it is obvious from the teachings of Pressley that the substrates could be alternatively moved with respect to the laser beam in the known apparatus of Miyachi et al.

Codama discloses a method of fabricating a thin film transistor which includes the steps of depositing an amorphous silicon layer; etching the silicon layer, the gate layer and the gate insulating layer; plasma doping the silicon layer to form source and drain regions, see column 1, lines 42-46; and hydrogenating the silicon layer. Therefore, in light of the semiconductor device manufacturing process of Codama, it would have been obvious to the skilled artisan to include a laser processing apparatus and an ion introducing apparatus in the known multi-chambered apparatus of Begin et al. in order to fabricate the thin film transistor of Codama.

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Although Codama teaches plasma doping of a semiconductor film, Codama fails to anticipate the use of an ion introducing apparatus comprising a grid electrode around which a dopant is made a plasma and accelerated toward the semiconductor layer. However, a plasma doping apparatus which comprises a grid electrode is known in the art, as taught by Kawachi et al., see Fig. 1. In addition, Kawachi et al. teach the activation of the implanted ions by laser annealing. Since Kawachi et al. teach that the disclosed ion introducing apparatus is applicable to polycrystalline thin film transistors and permits large-area implantation, it would have been obvious to one skilled in the art to include the ion introducing apparatus of Kawachi et al. in the known multi-chambered apparatus of Begin et al. in order to perform large-area doping or polysilicon in the fabrication of the device of Codama. Since Kawachi et al. teach laser annealing of the dopants implanted using the disclosed large-area doping apparatus shown in Fig. 1, it would have been obvious to the skilled artisan to include the ion introducing apparatus of Kawachi et al. in a multi-chambered apparatus that includes a laser irradiation chamber.

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Claims 56-61, 81 and 87 are rejected under 35 U.S.C. 103(a) as being unpatentable over Begin et al. in view of Miyachi et al., Nakayama et al., and Kawasaki et al., further in view of Codama, all of record, further in view of Hashizume, JP 03-286518, further in view of Kawachi et al., the article entitled "Large-Area Process for Fabrication of Poly-Si Thin Film Transistors Using Bucket Ion Source and XeCl Excimer Laser Annealing", newly cited.

Begin et al. disclose an apparatus for processing semiconductor wafers which includes satellite reaction chambers 60, 62, 64, and 66 disposed around the periphery of central chamber 14, see figure 1. A robot assembly 16 comprising arms 18, 20, and 22 is disposed in central chamber 14. Assembly 16 moves the substrate 12 to any position within the apparatus. Begin et al. lack anticipation only of disclosing that reaction chambers 60, 62, 64, and 66 comprise an ion introducing apparatus and a laser processing apparatus. However, apparatuses used for irradiating an amorphous silicon layer for dehalogenating and hydrogenating the layer, etching, and plasma doping are well known in the art, see Miyachi et al., Kawasaki et al., and Nakayama et al., respectively.

Miyachi et al., in particular, disclose an apparatus which comprises a film-forming chamber 1 for forming an amorphous semiconductor film and a dehalogenating-hydrogenating chamber 2, see figure 5, for example. The two chambers are combined by a conveying device 13. The substrates 10 move between the two chambers without being exposed to outside air. Note in Example 14 that the dehalogenation-hydrogenation is preferably

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performed by light irradiation using, for example, an ultraviolet laser, a visible light laser, or a carbon dioxide laser, see column 18, lines 29-43. Miyachi et al. lack anticipation only of using a rectangular-shaped laser beam having an elongated cross-section and of moving the substrate in a direction orthogonal to the laser beam during the irradiating step.

Hashizume discloses a laser processing apparatus which comprises a laser beam having a rectangular shape in which the laser beam is scanned by moving the beam relative to the substrate, see figure 2 and pages 7-9 of the translation. Note in figure 4, Hashizume shows that a rectangular-shaped laser beam having a width greater than a "substantially" square substrate is scanned along the substrate surface. The use of a rectangularly-shaped laser beam, as in the method of Hashizume, eliminates overlapping regions and permits uniform irradiation of the silicon layer, see page 7 of the translation. Since the rectangular-shaped laser beam of Hashizume permits uniform laser irradiation of semiconducting materials, it would have been obvious to one of ordinary skill in the art to use a laser beam having a rectangular crosssection in the dehalogenating-hydrogenating chamber of Miyachi et al. In addition, it is obvious from the teachings of Hashizume that the substrates could be moved with respect to the laser beam in the known apparatus of Miyachi et al. It is noted that Hashizume does not disclose the dimensions of either the substrate or laser beam, however, in light of the generic teaching of Hashizume to use a rectangularly-shaped laser beam having a width greater than that of the irradiated substrate, these dimensions are not deemed to patentably distinguish the claimed method from that of Hashizume. In addition, Hashizume discloses a laser beam scanning

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technique, however, it would have been obvious to one of ordinary skill in the art that, alternatively, the substrate could be moved with respect to the laser beam.

Codama discloses a method of fabricating a thin film transistor which includes the steps of depositing an amorphous silicon layer; etching the silicon layer, the gate layer and the gate insulating layer; plasma doping the silicon layer to form source and drain regions, see column 1, lines 42-46; and hydrogenating the silicon layer. Therefore, in light of the semiconductor device manufacturing process of Codama, it would have been obvious to the skilled artisan to include a laser processing apparatus and an ion introducing apparatus in the known multi-chambered apparatus of Begin et al. in order to fabricate the thin film transistor of Codama.

Although Codama teaches plasma doping of a semiconductor film, Codama fails to anticipate the use of an ion introducing apparatus comprising a grid electrode around which a dopant is made a plasma and accelerated toward the semiconductor layer. However, a plasma doping apparatus which comprises a grid electrode is known in the art, as taught by Kawachi et al., see Fig. 1. In addition, Kawachi et al. teach the activation of the implanted ions by laser annealing. Since Kawachi et al. teach that the disclosed ion introducing apparatus is applicable to polycrystalline thin film transistors and permits large-area implantation, it would have been obvious to one skilled in the art to include the ion introducing apparatus of Kawachi et al. in the known multi-chambered apparatus of Begin et al. in order to perform large-area doping or polysilicon in the fabrication of the device of Codama. Since Kawachi et al. teach laser annealing of the dopants implanted using the disclosed large-area doping apparatus shown in Fig. 1, it

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would have been obvious to the skilled artisan to include the ion introducing apparatus of Kawachi et al. in a multi-chambered apparatus that includes a laser irradiation chamber.

Claims 74-79, 82-85, and 89-91 are rejected under 35 U.S.C. 103(a) as being unpatentable over Begin et al. in view of Miyachi et al., Nakayama et al., and Kawasaki et al., further in view of Codama, all of record, further in view of Kawachi et al., the article entitled "Large-Area Process for Fabrication of Poly-Si Thin Film Transistors Using Bucket Ion Source and XeCl Excimer Laser Annealing", newly cited.

Begin et al. disclose an apparatus for processing semiconductor wafers which includes satellite reaction chambers 60, 62, 64, and 66 disposed around the periphery of central chamber 14, see figure 1. A robot assembly 16 comprising arms 18, 20, and 22 is disposed in central chamber 14. Assembly 16 moves the substrate 12 to any position within the apparatus. Begin et al. lack anticipation only of disclosing that reaction chambers 60, 62, 64, and 66 comprise an ion introducing apparatus and a laser processing apparatus. However, apparatuses used for irradiating an amorphous silicon layer for dehalogenating and hydrogenating the layer, etching, and plasma doping are well known in the art, see Miyachi et al., Kawasaki et al., and Nakayama et al., respectively.

Miyachi et al., in particular, disclose an apparatus which comprises a film-forming chamber 1 for forming an amorphous semiconductor film and a dehalogenating-hydrogenating chamber 2, see figure 5, for example. The two chambers are combined by a conveying

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device 13. The substrates 10 move between the two chambers without being exposed to outside air. Note in Example 14 that the dehalogenation-hydrogenation is preferably performed by light irradiation using, for example, an ultraviolet laser, a visible light laser, or a carbon dioxide laser, see column 18, lines 29-43. Miyachi et al. lack anticipation only of using a rectangular-shaped laser beam having an elongated cross-section and of moving the substrate in a direction orthogonal to the laser beam during the irradiating step.

Codama discloses a method of fabricating a thin film transistor which includes the steps of depositing an amorphous silicon layer; etching the silicon layer, the gate layer and the gate insulating layer; plasma doping the silicon layer to form source and drain regions, see column 1, lines 42-46; and hydrogenating the silicon layer. Therefore, in light of the semiconductor device manufacturing process of Codama, it would have been obvious to the skilled artisan to include a laser processing apparatus and an ion introducing apparatus in the known multi-chambered apparatus of Begin et al. in order to fabricate the thin film transistor of Codama.

Although Codama teaches plasma doping of a semiconductor film, Codama fails to anticipate the use of an ion introducing apparatus comprising a grid electrode around which a dopant is made a plasma and accelerated toward the semiconductor layer. However, a plasma doping apparatus which comprises a grid electrode is known in the art, as taught by Kawachi et al., see Fig. 1. In addition, Kawachi et al. teach the activation of the implanted ions by laser annealing. Since Kawachi et al. teach that the disclosed ion introducing apparatus is applicable to polycrystalline thin film transistors and permits large-area implantation, it would have been

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obvious to one skilled in the art to include the ion introducing apparatus of Kawachi et al. in the known multi-chambered apparatus of Begin et al. in order to perform large-area doping or polysilicon in the fabrication of the device of Codama. Since Kawachi et al. teach laser annealing of the dopants implanted using the disclosed large-area doping apparatus shown in Fig. 1, it would have been obvious to the skilled artisan to include the ion introducing apparatus of Kawachi et al. in a multi-chambered apparatus that includes a laser irradiation chamber.

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Response to Arguments

Applicants' arguments filed January 31, 2001, have been fully considered but they are not persuasive.

Applicants have argued that Kawachi et al. do not disclose, suggest or motivate conducting the laser activation in a chamber. Admittedly, Kawachi et al. do not specifically disclose that the laser activation step is performed in a chamber. Firstly, independent claims 16, 56, 74, 75, 76, and 77 do not require conducting the laser processing in a chamber. The claims merely require "a laser processing apparatus". Hence, this argument is not persuasive. Secondly, it is well known in the art to perform laser annealing in a chamber, as evidenced by Miyachi et al. It is well within the purview of the skilled artisan to recognize that the laser activation step of Kawachi et al. could have been performed in a chamber regardless of whether or not Kawachi et al. expressly discloses performing the laser activation in a chamber.

Applicants have further argued that in the presently claimed apparatus doping is conducted for a thin film transistor, whereas Miyachi teaches a solar cell. However, it has been well established that intended use is not germane in determining the patentability of an apparatus. *In re Finsterwalder* 168 USPQ 530 (CCPA 1971). The purpose to which an apparatus is to be put and any expression relating the apparatus to contents thereof during the intended operation are not significant in determining the patentability of an apparatus claim. *Ex parte Thibault* 164 USPQ 666 (PTO Board of Appeals 1969). A recitation with respect to the manner in which a claimed

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apparatus is intended to be employed does not differentiate the claimed apparatus from a prior art apparatus satisfying the structural limitation of that claimed. Ex parte Marsham 2 USPQ2d 1647 (PTO Board of Appeals 1987).

Applicants further argue that none of the cited references disclose, suggest or motivate to combine a doping chamber and a laser irradiation chamber in a multi-chambered apparatus, as presently claimed. Admittedly, none of the cited references disclose a multi-chambered apparatus comprising an ion introducing apparatus and a laser processing apparatus. However, multi-chambered semiconductor processing apparatuses are well known in the art because these apparatuses permit the fabrication of semiconductor devices without exposing the devices to air and possible contamination, see the multi-chambered apparatus of Begin et al. Likewise, it is well known to fabricate semiconductor devices by using processes such as plasma doping and laser annealing, see Codama and Kawachi et al. Hence, it would have been obvious to one skilled in the art to fabricate the transistor of Codama in a multi-chambered apparatus similar to the one of Begin et al. To practice the fabrication process of Codama in the multi-chambered apparatus of Begin et al., an ion doping apparatus and a laser processing apparatus would necessarily have to be included in the multi-chambered apparatus of Begin et al.

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Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to M. Wilczewski whose telephone number is (703) 308-2771.

M. Wilczewski Primary Examiner Tech Center 2800

MW

April 23, 2001